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APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. 10/623,912 07/21/2003 Yue-Der Chih TS01-1584 5364 06/04/2004 **EXAMINER** GEORGE O. SAILE WOJCIECHOWICZ, EDWARD JOSEPH STEPHEN B. ACKERMAN 28 DAVIS AVENUE ART UNIT PAPER NUMBER POUGHKEEPSIE, NY 12603

2815 DATE MAILED: 06/04/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Agaliagtica Ma	Up.
Office Action Summary	Application No.	Applicant(s)
	10/623,912	CHIH ET AL
	Examiner	Art Unit
	E Wojciechowicz	2815
The MAILING DATE of this communication a Period for Reply	ppears on the cover sheet wi	th the correspondence address -
A SHORTENED STATUTORY PERIOD FOR REP THE MAILING DATE OF THIS COMMUNICATION Extensions of time may be available under the provisions of 37 CFR to after SIX (6) MONTHS from the mailing date of this communication. If the period for reply especified above is less than thirty (30) days, a relifinor period for reply is specified above; the maximum statutory periodic facility is provided by the Office later than three months after the mail earned patent term adjustment. See 37 CFR 1.704(b).	I. 1.136(a). In no event, however, may a nepty within the statutory minimum of thirt d'will apply and will expire SIX (6) MON the cause the application to become AB	eply be timely filed y (30) days will be considered timely. THS from the mailing date of this communication.
Status		
1) Responsive to communication(s) filed on		•
3) Since this application is in condition for allow		ers, prosecution as to the morite in
closed in accordance with the practice under	Ex parte Quayle, 1935 C.D.	. 11, 453 O.G. 213
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Disposition of Claims		
4) Claim(s) 1-36 is/are pending in the application		
4a) Of the above claim(s) is/are withdra	awn from consideration.	
5) Claim(s) is/are allowed.		• •
6)⊠ Claim(s) <u>1-36</u> is/are rejected.		
7) Claim(s) is/are objected to.		
8) Claim(s) are subject to restriction and/	or election requirement.	
pplication Papers	•	*
9) The specification is objected to by the Examin	er.	
10) The drawing(s) filed on is/are: a) ac		v the Examiner
Applicant may not request that any objection to the	e drawing(s) be held in abeyand	ce See 37 CFR 1 85/a)
Replacement drawing sheet(s) including the correct		
11) The oath or declaration is objected to by the E	xaminer. Note the attached	Office Action or form PTO-152
riority under 35 U.S.C. § 119		
12) ACKNOWledgment is made of a claim for foreign	n priority under 35 U.S.C. §	119(a)-(d) or (f).
12) Acknowledgment is made of a claim for foreign		
a) All b) Some * c) None of:		
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Application/Control Number: 10/623,912
Art Unit: 2815

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-36 are rejected under 35 U.S.C. 102(b) as being anticipated by Georgakos et al. The inventive structure and method is taught by the Georgakos reference. For example, comparing the claim 1 recitation with the Fig. 4 structure of Georgakos, it is seen that Georgakos also forms a memory array having a select gate MOSFET (T2) and a split gate MOSFET (T1) whose source is connected to the drain of the select gate MOSFET.

In addition, the Georgakos reference teaches bit lines (BL) which are connected to the drains of the split gate device (Fig. 4); source lines (SL) which are tied to the sources of the select gate devices; word lines (WL) tied to the control gates (CG) of the split gate memory cell; select lines (SLST) tied to select gates of the select gate device; floating gates which are insulated from the control gate (col. 3, I.1-5); common source/drain regions between the split gate device and the select gate device (Fig. 4); each of the floating and control gates insulated from each other, and the source/drain regions (Fig. 1A).

In addition, Georgakos also teaches the same biasing arrangements as claimed. For example, see Tables 1-3, along with the discussion at columns 3 through 5.

Likewise, Georgakos teaches the specific process steps recited in the claims, which correspond to the structural elements recited above.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to E Wojciechowicz whose telephone number is (571) 272-1739. The examiner can normally be reached on Monday through Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1739. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

E Wojciechowicz Primary-Examiner Art Unit 2815

EW: ew